

## Super Low Distortion Multilayer Ceramic Capacitors

## GMK212SD153KD-T



## ■ Features

- Item Summary  
0.015uF±10%, 35V,  
Standard, 0805/2012 (EIA/JIS)
- Lifecycle Stage  
Mass Production
- Standard packaging quantity (minimum)  
Taping paper 4000pcs

## ■ Products characteristics table

Capacitance	0.015 uF ± 10 %
Case Size (EIA/JIS)	0805/2012
Rated Voltage	35 V
tan δ (max)	0.1 %
Operating Temp. Range (EIA)	-55 to +125 °C
Operating Temp. Range (JIS)	-55 to +125 °C
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	10 GΩ
RoHS2 Compliance (10 subst.)	Yes
REACH Compliance (173 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow

## ■ External Dimensions

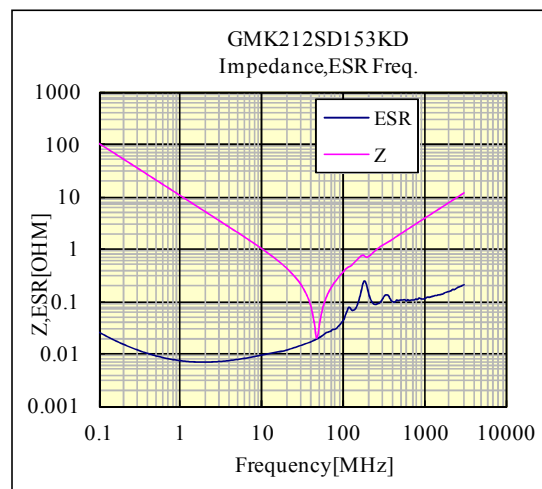
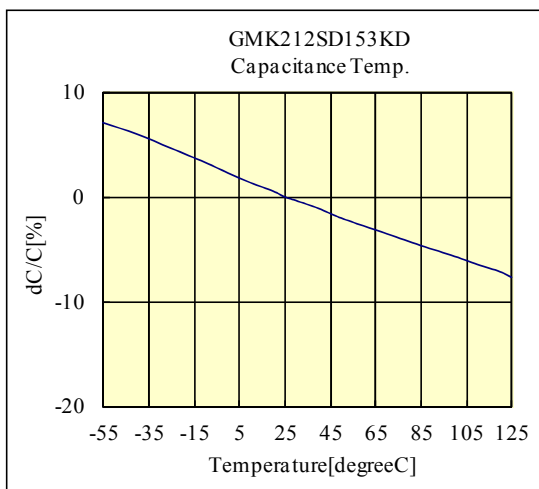
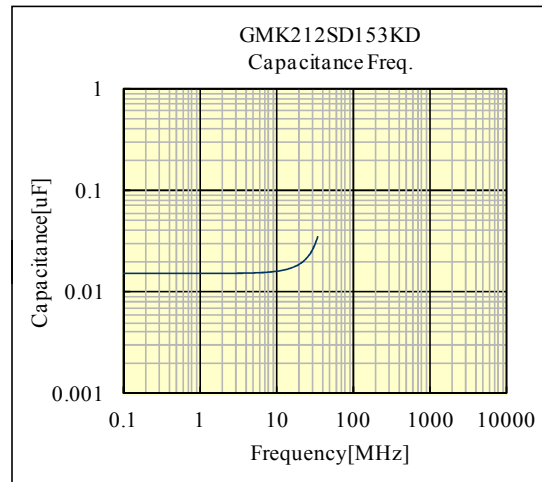
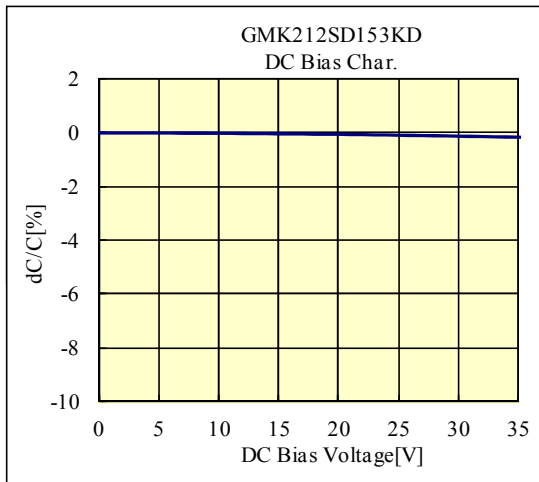
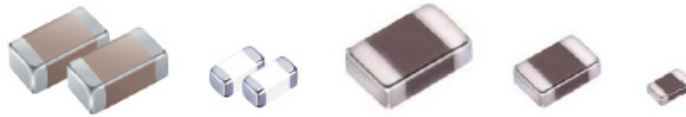
Dimension L	2.0 ±0.10 mm
Dimension W	1.25 ±0.10 mm
Dimension T	0.85 ±0.10 mm
Dimension e	0.50 ±0.25 mm

2017.04.30

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.  
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 Before making final selection, please check product specification.

## ○GMK212SD153KD

**35V 2012 0.015uF +/- 10% t=0.95mmMAX**  
**35V 0805 0.015uF +/- 10% t=0.037inchMAX**



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